

**Patent Claims**

1. An integrated circuit arrangement which has:  
an output circuit having at least one first output  
5 connection which can provide a data signal;  
at least one first data output connection,  
with the at least first output connection and the at  
least one data output connection having at least one  
first inductance connected between them.  
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2. The integrated circuit arrangement as claimed in  
claim 1, where the output circuit has a second output  
connection and a second data output connection, said  
second output connection and said second data output  
15 connection having at least one second inductance  
connected between them.
3. The integrated circuit arrangement as claimed in  
claim 2, where the first inductance is in a form such  
20 that it forms a first frequency filter having a  
prescribed frequency band together with the first data  
output connection, and the second inductance is in a  
form such that it forms a second frequency filter  
having the prescribed frequency band together with the  
25 second data output connection.
4. The integrated circuit arrangement as claimed in  
claim 3, where the prescribed frequency band is in a  
range from 1 GHz to 100 GHz.  
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5. The integrated circuit arrangement as claimed in  
one of claims 1 to 4, which has a plurality of  
frequency filters coupled in series between the at  
least first output connection and the at least first  
35 data output connection.
6. The integrated circuit arrangement as claimed in  
one of claims 2 to 5, where the output circuit is set

up such that a differential data signal can be provided at the first output connection and at the second output connection.

5    7.    The integrated circuit arrangement as claimed in claim 6, where the at least one first inductance is coupled to the at least one second inductance.

8.    The integrated circuit arrangement as claimed in  
10 one of claims 1 to 7, where at least one of the inductances is a monolithically integrated inductance.

9.    The integrated circuit arrangement as claimed in  
one of claims 1 to 8, where the output circuit has a  
15 differential amplifier.

10.   The integrated circuit arrangement as claimed in one of claims 1 to 8, where the output circuit has a multiplexer.